



	Type	L #	Hits	Search Text	DBs	Time Stamp	C	Er	Erro
1	BRS	L2	8832	(memory or (storage adj device)) and (oscillatS3 same digitalS2) and controlS4 and (load or lamp)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 09:33			
2	BRS	L3	5432	(memory or (storage adj device)) and (oscillatS3 same digitalS2) and (controlS4 same (load or lamp))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 09:12			
3	BRS	L4	4656	3 and ((power near2 switchS3) or drivS3 or (switchS3 near2 device))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 09:33			
4	BRS	L5	674	4 and (set near2 point)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 09:34			
5	BRS	L6	433	4 and (set adj point)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 09:15			
6	BRS	L7	135	6 and (minimS2 near3 frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 09:35			
7	BRS	L8	125	7 and (IC or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 10:41			
8	BRS	L9	4629	(memory or (storage adj device)) and (oscillatS3 same digitalS2) and controller and (load or lamp)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 09:33			
9	BRS	L10	4083	9 and ((power near2 switchS3) or drivS3 or (switchS3 near2 device))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 09:33			
10	BRS	L11	1000	10 and ((set near2 point) or (referenceS near2 level))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 14:09			
11	BRS	L12	312	11 and (minimS2 near3 frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 10:09			
12	BRS	L14	9	13 and (load or lamp)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 10:39			
13	BRS	L13	13	((digitalS2 near3 oscillator) same ((set adj point) or (reference adj level))) and (memory or (storage adj device))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 11:46			
14	BRS	L15	331	(digitalS2 near3 oscillator) and ((set adj point) or (reference adj level)) and (memory or (storage adj device))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 10:39			
15	BRS	L17	200	15 and load	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 10:41			
16	BRS	L19	168	17 and (IC or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 10:41			
17	BRS	L18	171	16 and (IC or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 11:28			
18	BRS	L16	208	15 and (load or lamp)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 11:40			
19	BRS	L28	3405	(ballast near2 controlS4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 11:41			
20	BRS	L30	371	28 and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 11:42			
21	BRS	L31	4	30 and (digital near2 oscillator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 11:46			
22	BRS	L33	1	28 and ((digitalS2 near3 oscillator) same ((set adj point) or (reference adj level))) and (memory or (storage adj device))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 11:46			
23	BRS	L32	85	30 and oscillator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 12:27			
24	BRS	L35	208	(digital near2 controlS3 near2 oscillator) and (memory or (storage adj device)) and controller	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 13:56			
25	BRS	L36	105	35 and (lamp or loadS3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 12:38			
26	BRS	L37	153	(digital near2 controlS3 near2 oscillator) and (memory or (storage adj device)) and (lamp or load)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 14:28			
27	BRS	L38	1	37 and (ballast same controlS4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 13:57			
28	BRS	L39	2	37 and ballast and controlS4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 14:09			
29	BRS	L40	2	37 and ballast	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 14:09			
30	BRS	L41	2	40 and ((set near2 point) or (referenceS near2 level))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 14:10			
31	BRS	L42	70	(digital near2 controlS3 near2 oscillator) and (memory or (storage adj device)) and ((lamp or load) with controlS4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	2005/07/19 14:28			